

logic of data(binary)  
D8,7,8,5,4,3,2,1,0

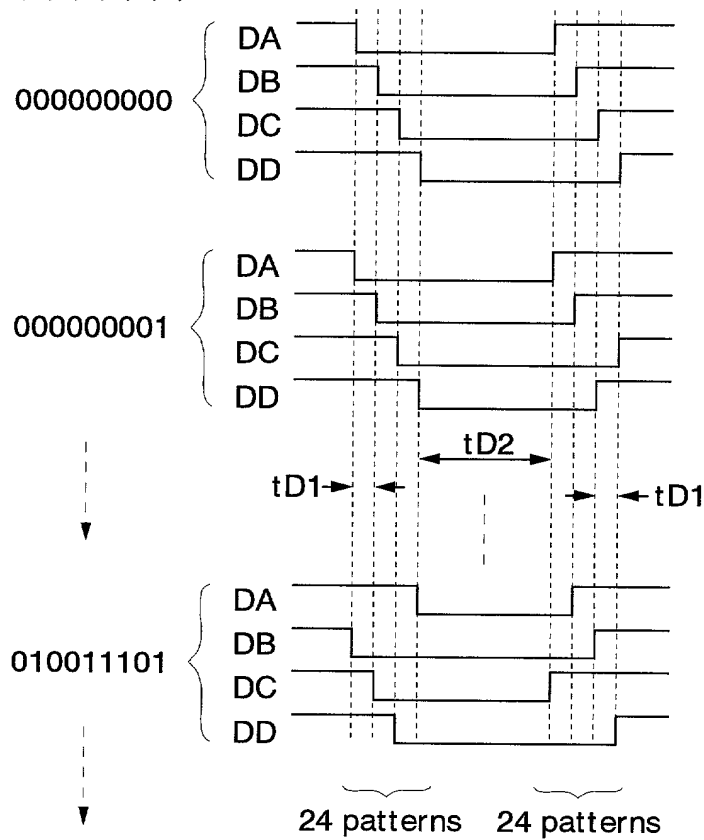


Fig. 1

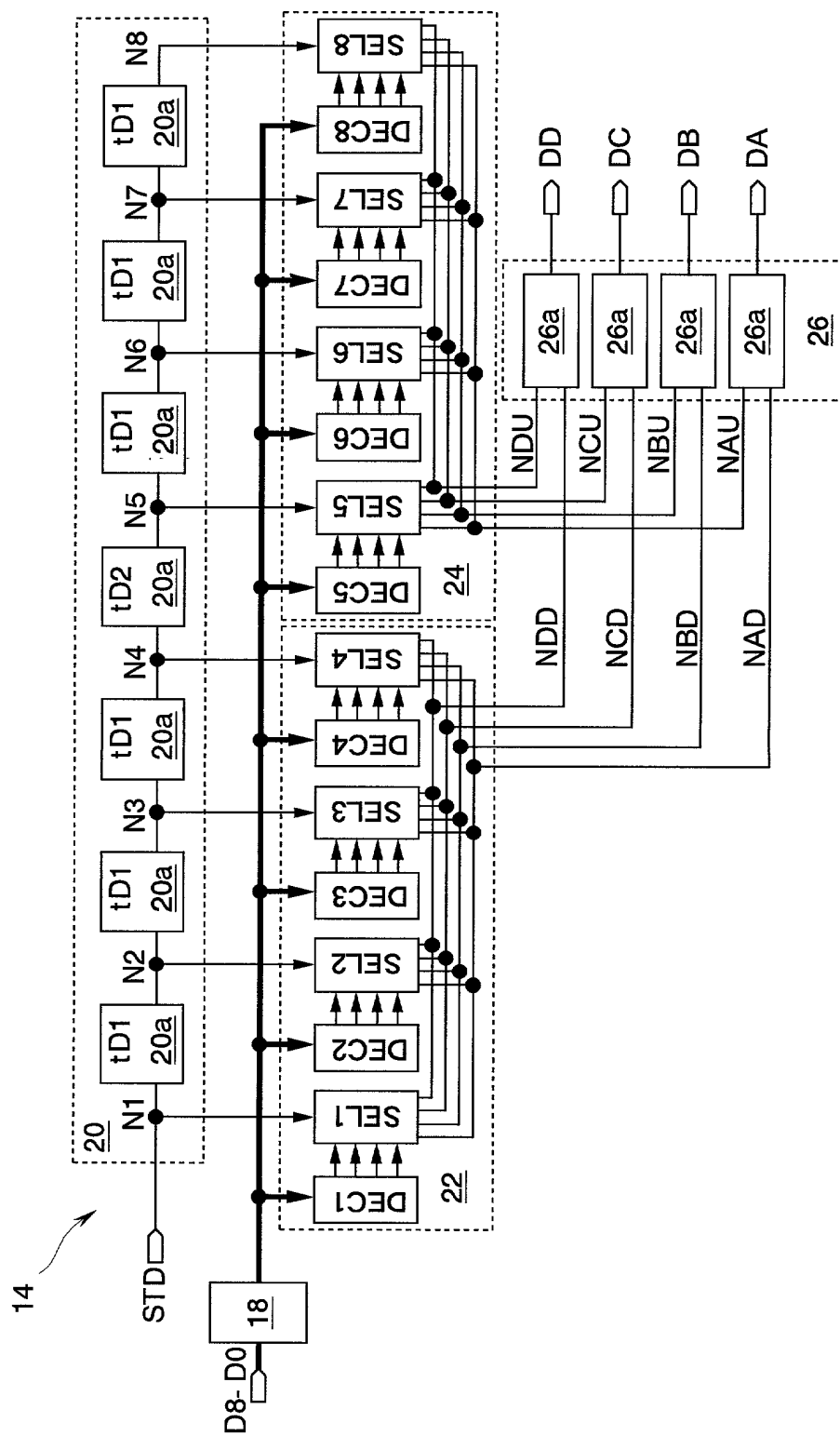


Fig. 2

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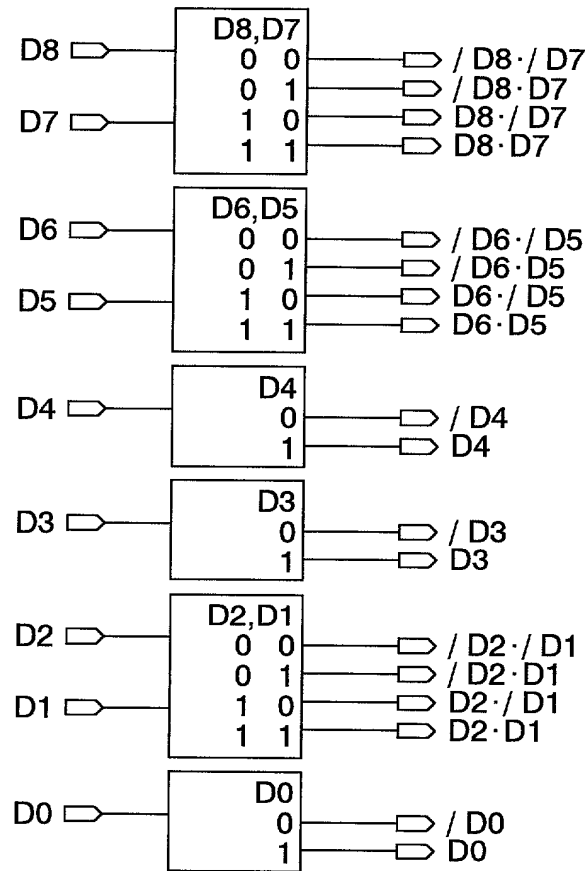


Fig. 3

No.	order of edges	for leading edge				for trailing edge			
		logic L1				logic L2			
		D8,7	D6,5	D4		D8,7	D6,5	D4	D3
0	ABCD	00	00	0		11	00	0	0
1	ABDC	00	00	1		11	00	1	0
2	ACBD	00	01	0		11	01	0	0
3	ACDB	00	01	1		11	01	1	0
4	ADBC	00	10	0		11	10	0	0
5	ADCB	00	10	1		11	10	1	0
6	BACD	00	11	0		11	11	0	0
7	BADC	00	11	1		11	11	1	0
8	BCAD	01	00	0		11	00	0	1
9	BCDA	01	00	1		11	00	1	1
10	BDAC	01	01	0		11	01	0	1
11	BDCA	01	01	1		11	01	1	1
12	CABD	01	10	0		11	10	0	1
13	CADB	01	10	1		11	10	1	1
14	CBAD	01	11	0		11	11	0	1
15	CBDA	01	11	1		11	11	1	1
16	CDAB	10	00	0		11	-	00	0
17	CDBA	10	00	1		11	-	00	1
18	DABC	10	01	0		11	-	01	0
19	DACB	10	01	1		11	-	01	1
20	DBAC	10	10	0		11	-	10	0
21	DBCA	10	10	1		11	-	10	1
22	DCAB	10	11	0		11	-	11	0
23	DCBA	10	11	1		11	-	11	1

Fig. 4

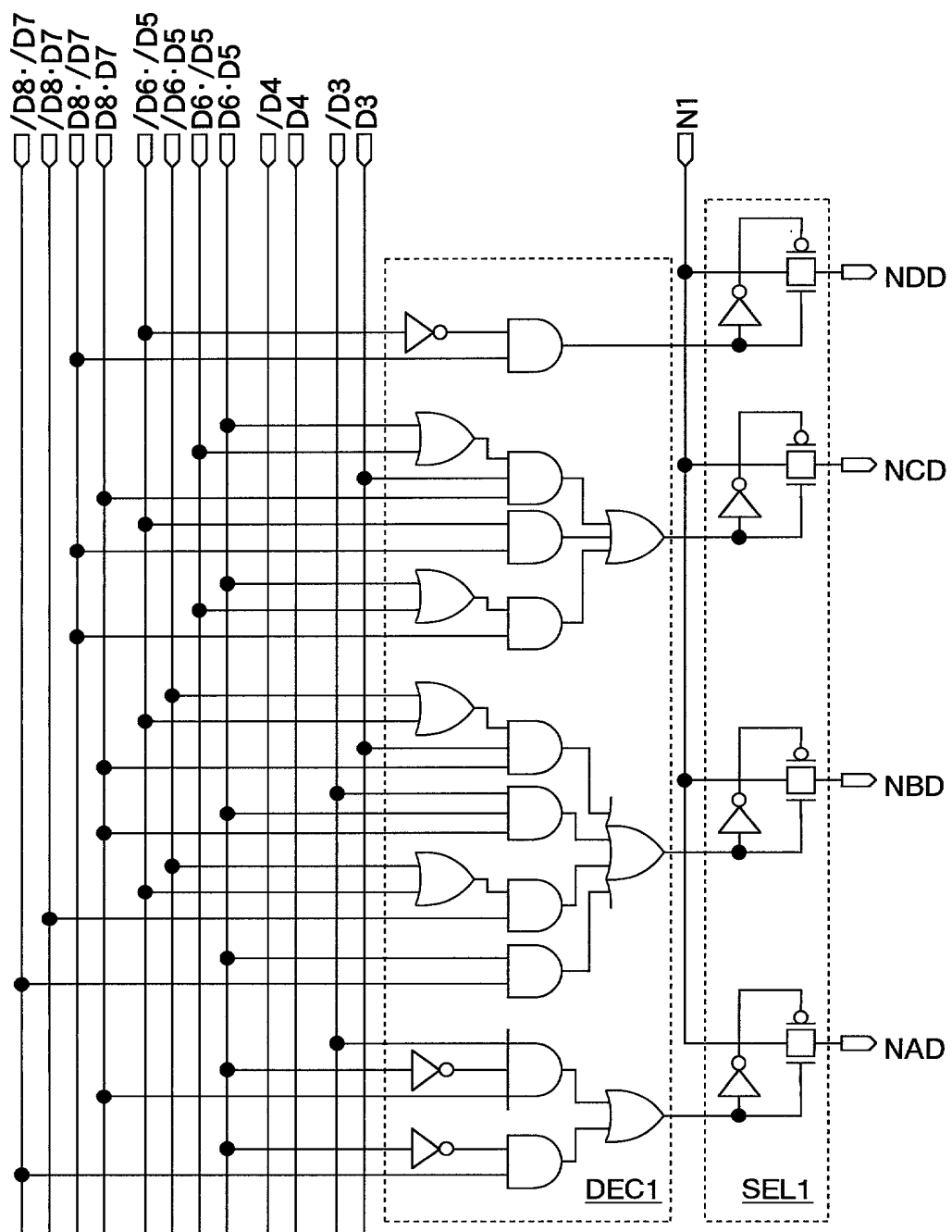


Fig. 5

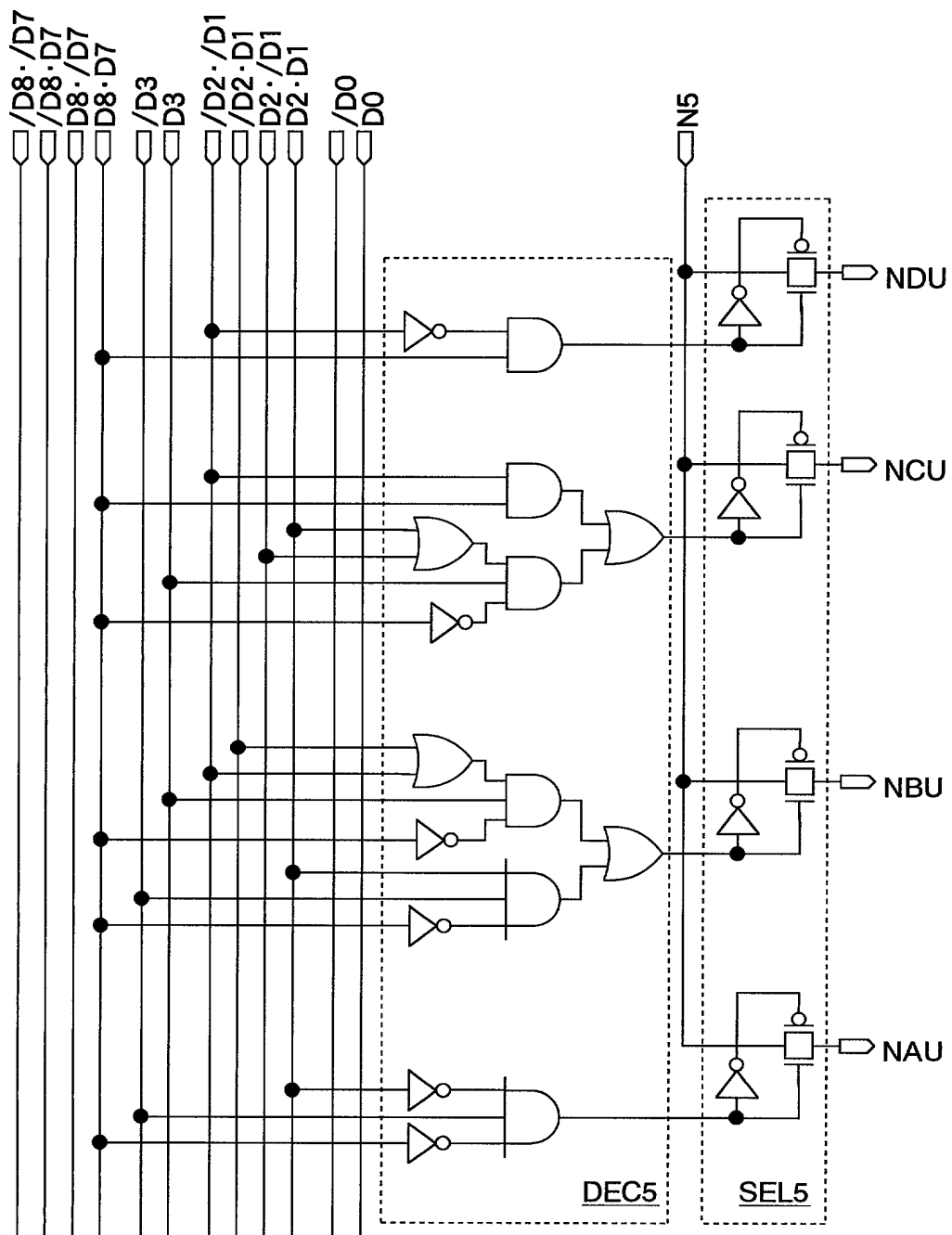


Fig. 6

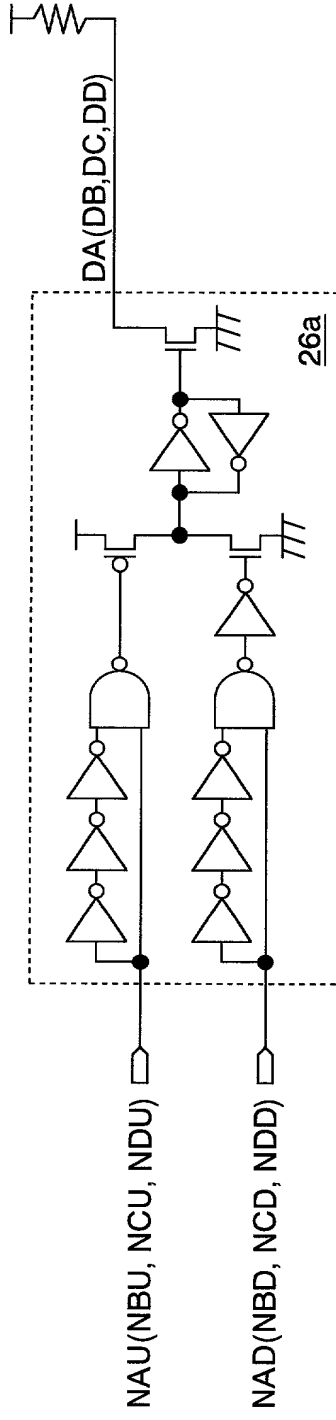
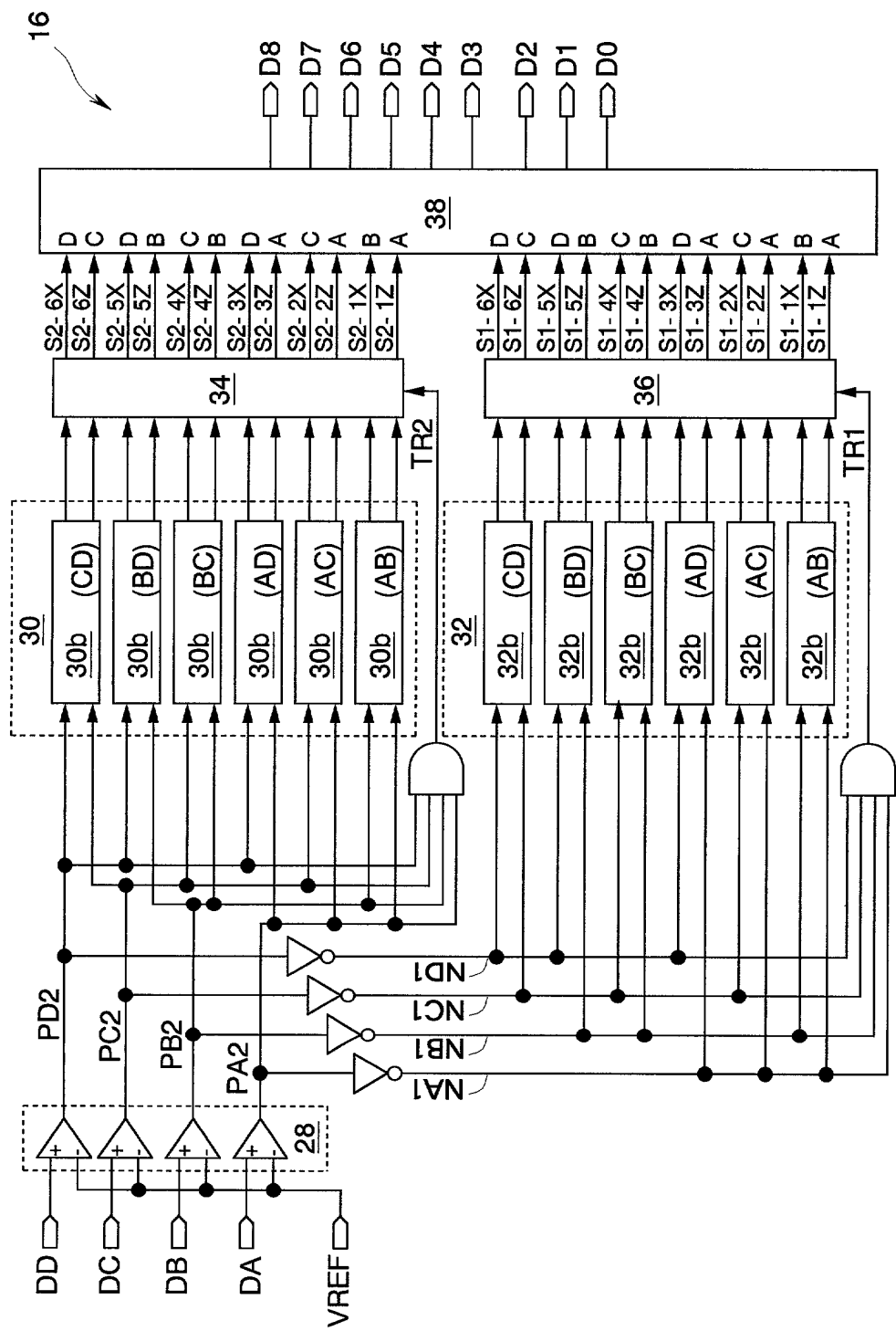


Fig. 7



**Fig. 8**

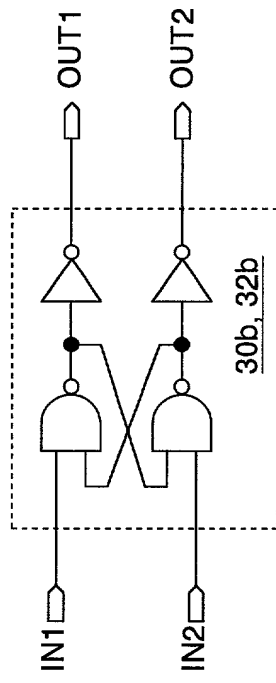


Fig. 9

		for leading edge										for trailing edge									
No.	order of edges	output OUT of comparator 32b 1,2,3,4,5,6										output OUT of comparator 30b 1,2,3,4,5,6									
		logic L1					logic L2					logic L1					logic L2				
0	ABCD	00	00	0	0	0	11	00	0	0	0	11bar	0	00	0	0	11	-	00	0	0
1	ABDC	00	00	1	0	0	11	00	1	0	0	11bar	0	00	1	0	11	-	00	1	0
2	ACBD	00	01	0	0	0	11	01	0	0	0	11bar	0	01	0	0	11	-	01	0	0
3	ACDB	00	01	1	0	0	11	01	1	0	0	11bar	0	01	1	0	11	-	01	1	0
4	ADBC	00	10	0	0	0	11	10	0	0	0	11bar	0	10	0	0	11	-	10	0	0
5	ADCB	00	10	1	0	0	11	10	1	0	0	11bar	0	10	1	0	11	-	10	1	0
6	BACD	00	11	0	0	0	11	11	0	0	0	11bar	0	11	0	0	11	-	11	0	0
7	BADC	00	11	1	0	0	11	11	1	0	0	11bar	0	11	1	0	11	-	11	1	0
8	BCAD	01	00	0	0	0	11	00	0	1	0	11bar	1	00	0	0	11	-	00	0	0
9	BCDA	01	00	1	0	0	11	00	1	0	0	11bar	1	00	1	0	11	-	00	1	0
10	BDAC	01	01	0	0	0	11	01	0	0	0	11bar	1	01	0	0	11	-	01	0	0
11	BDCA	01	01	1	0	0	11	01	1	0	0	11bar	1	01	1	0	11	-	01	1	0
12	CABD	01	10	0	0	0	11	10	0	0	0	11bar	1	10	0	0	11	-	10	0	0
13	CADB	01	10	1	0	0	11	10	1	0	0	11bar	1	10	1	0	11	-	10	1	0
14	CBAD	01	11	0	0	0	11	11	0	0	0	11bar	1	11	0	0	11	-	11	0	0
15	CBDA	01	11	1	0	0	11	11	1	0	0	11bar	1	11	1	0	11	-	11	1	0
16	CDAB	10	00	0	0	0	11	00	0	0	0	11	-	00	0	0	11	-	00	0	0
17	CDBA	10	00	1	0	0	11	00	1	0	0	11	-	00	1	0	11	-	00	1	0
18	DABC	10	01	0	0	0	11	01	0	0	0	11	-	01	0	0	11	-	01	0	0
19	DACB	10	01	1	0	0	11	01	1	0	0	11	-	01	1	0	11	-	01	1	0
20	DBAC	10	10	0	0	0	11	10	0	0	0	11	-	10	0	0	11	-	10	0	0
21	DBCA	10	10	1	0	0	11	10	1	0	0	11	-	10	1	0	11	-	10	1	0
22	DCAB	10	11	0	0	0	11	11	0	0	0	11	-	11	0	0	11	-	11	0	0
23	DCBA	10	11	1	0	0	11	11	1	0	0	11	-	11	1	0	11	-	11	1	0

Fig. 10

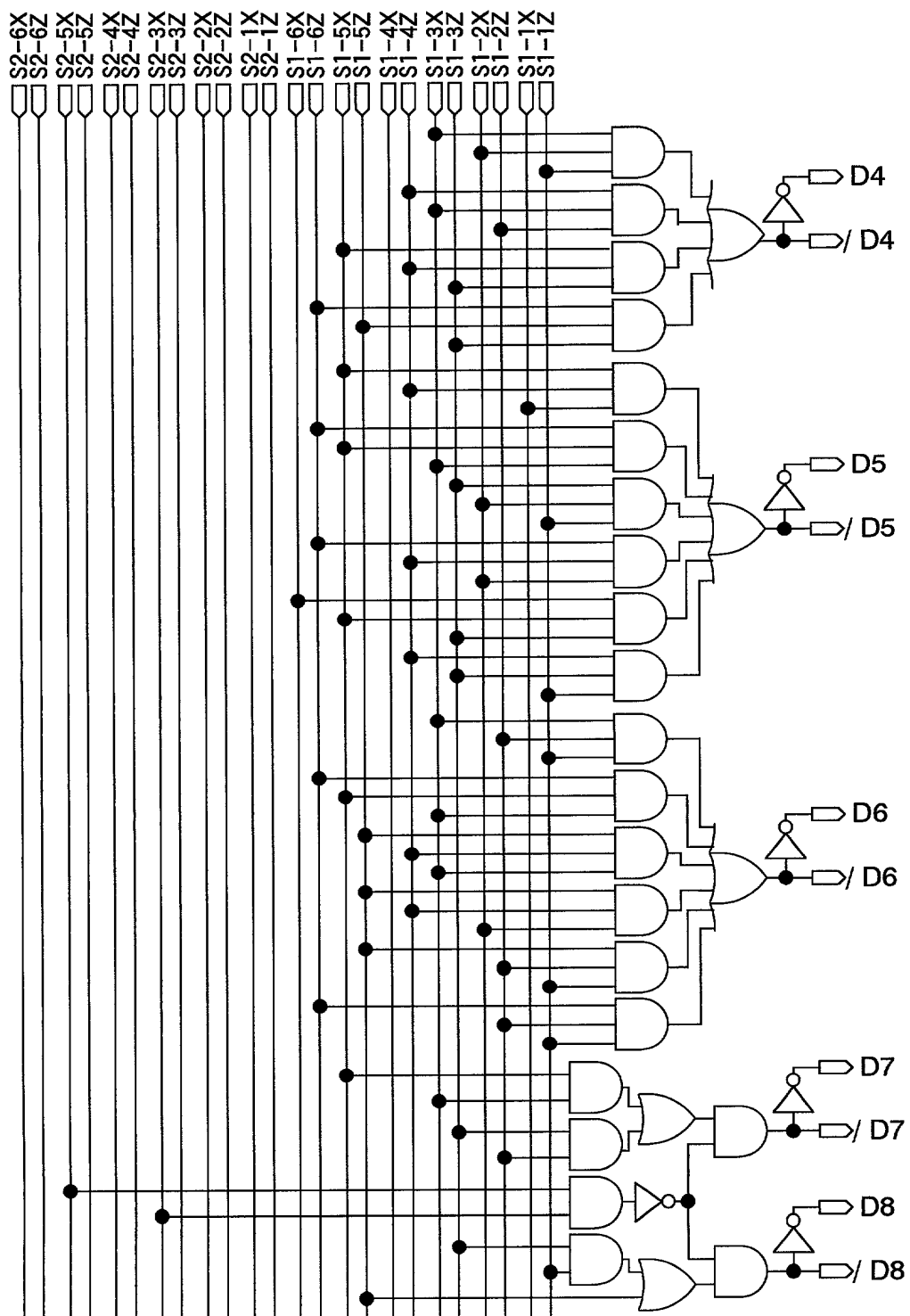


Fig. 11

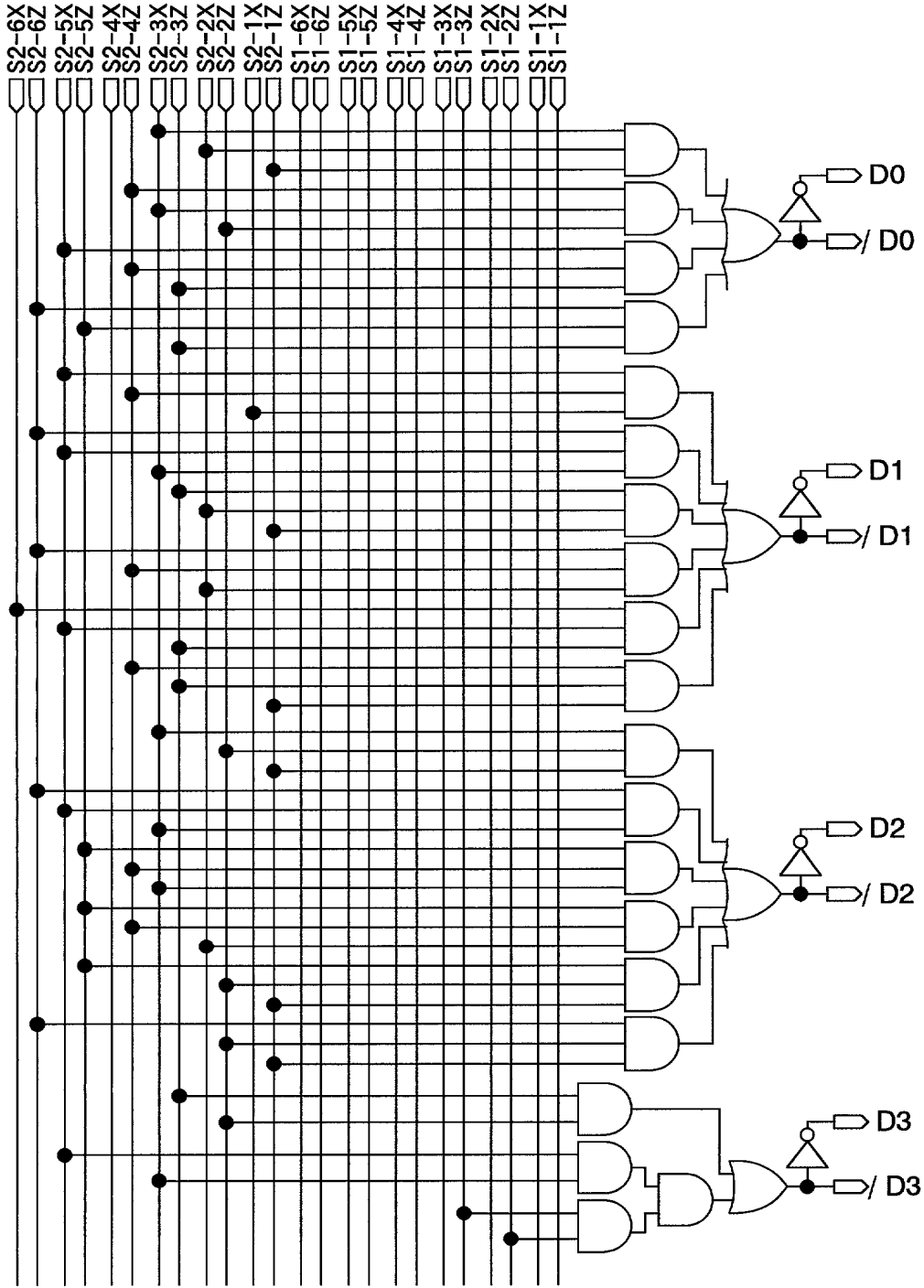
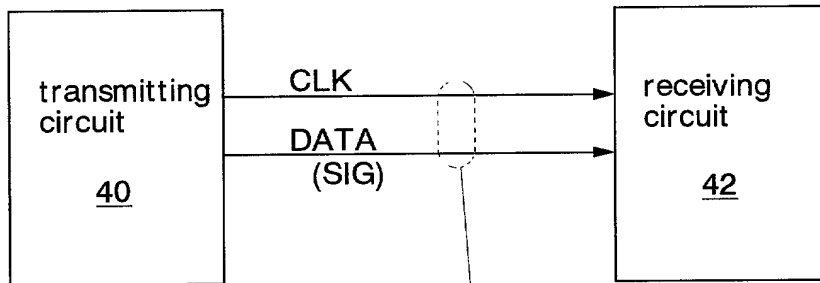


Fig. 12



logic of data(binary)  
DT1,0

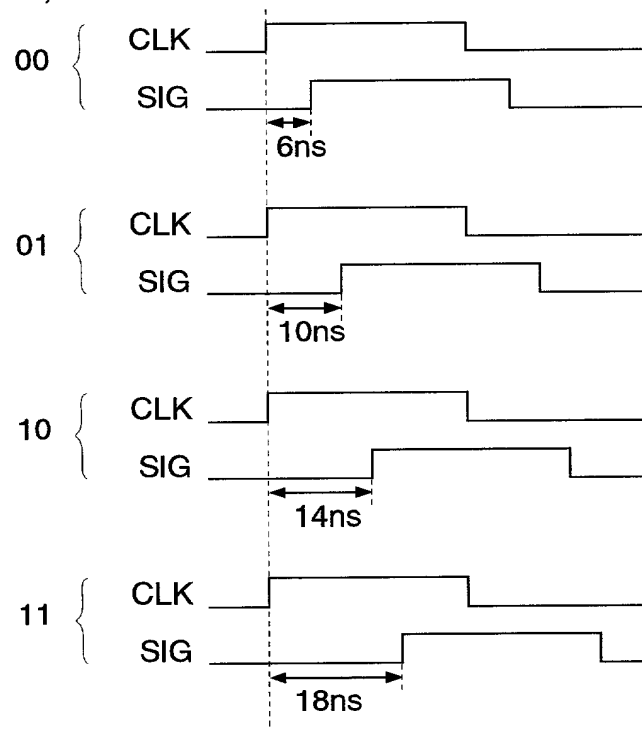


Fig. 13

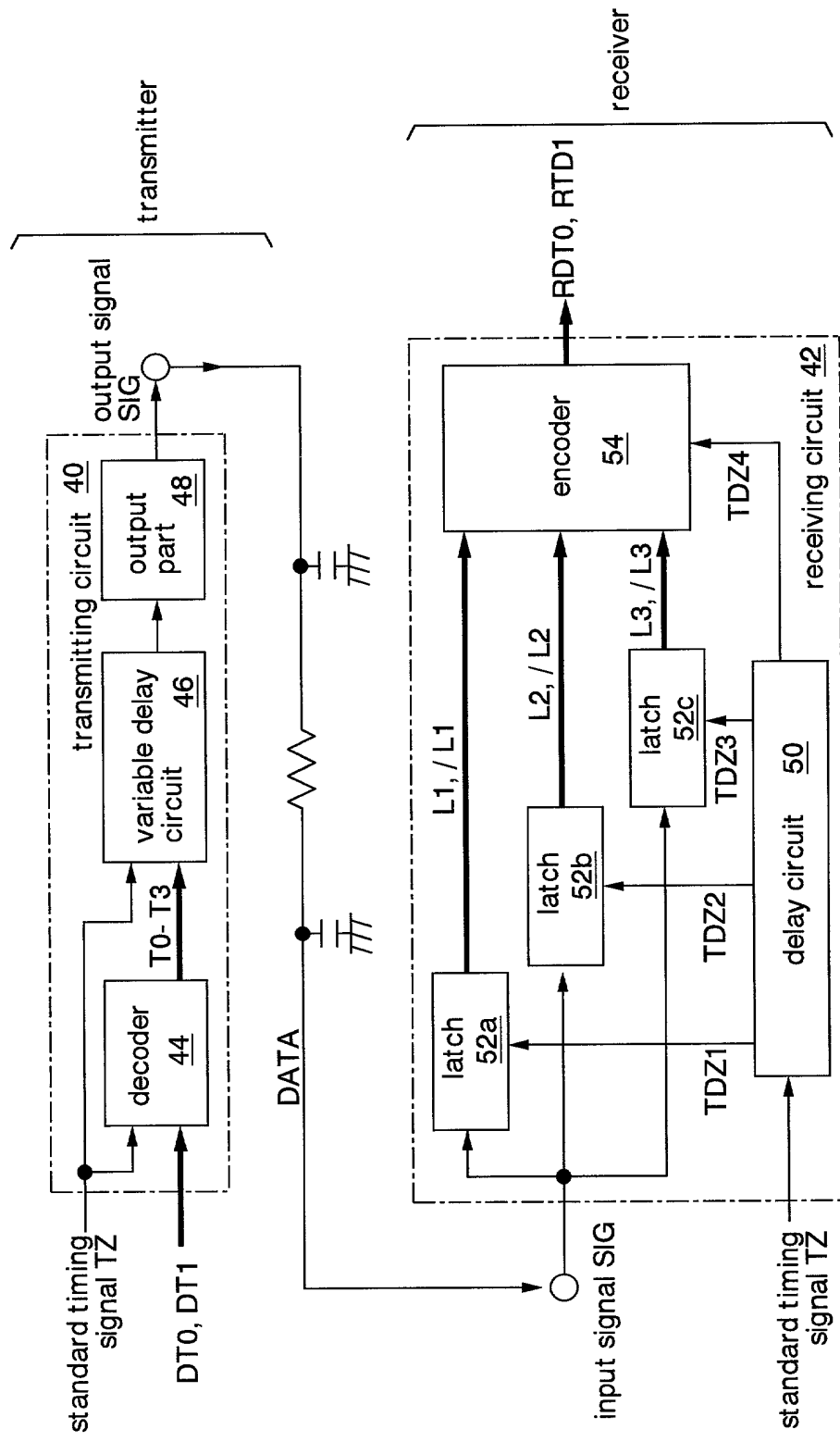


Fig. 14

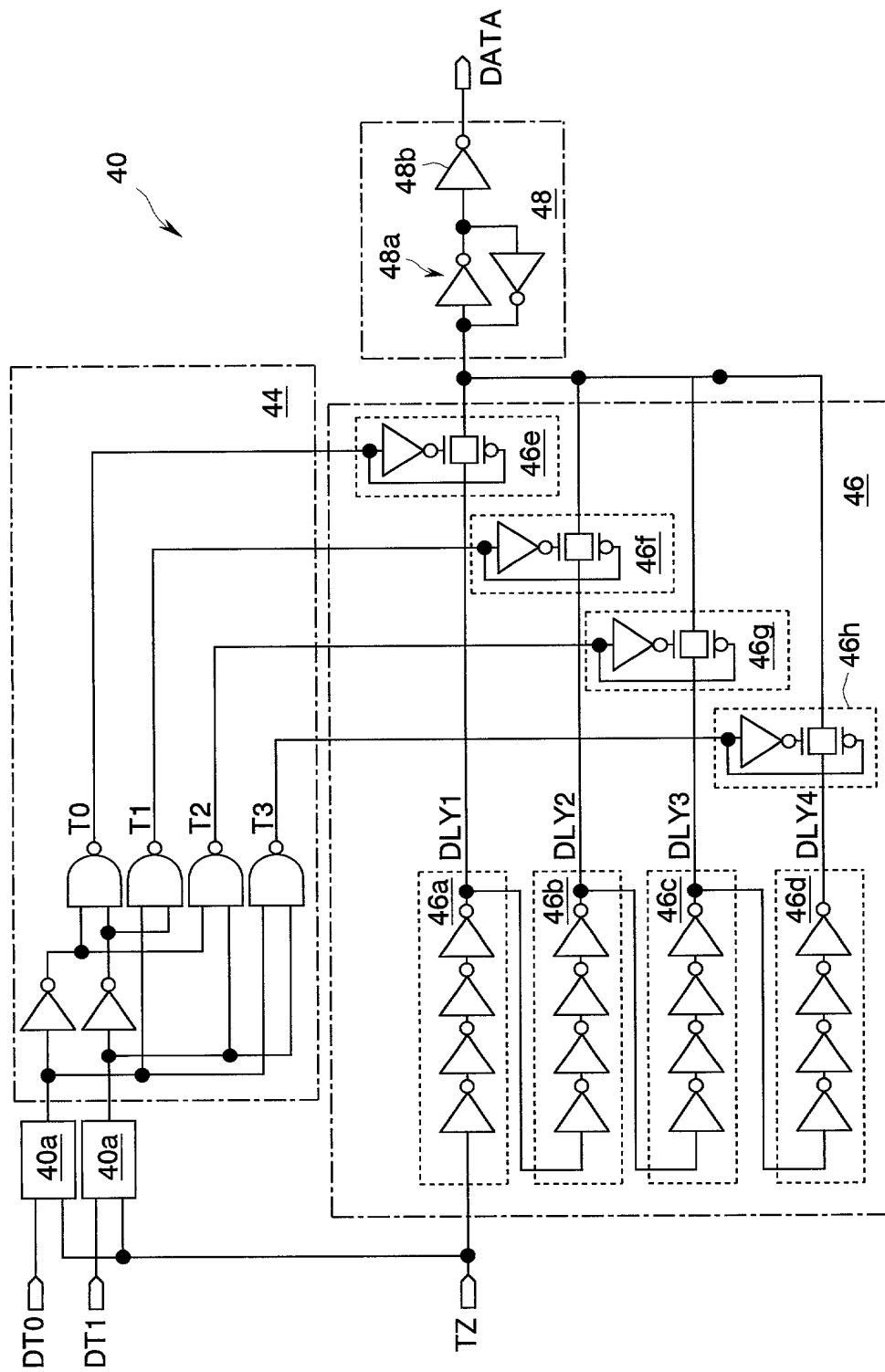


Fig. 15

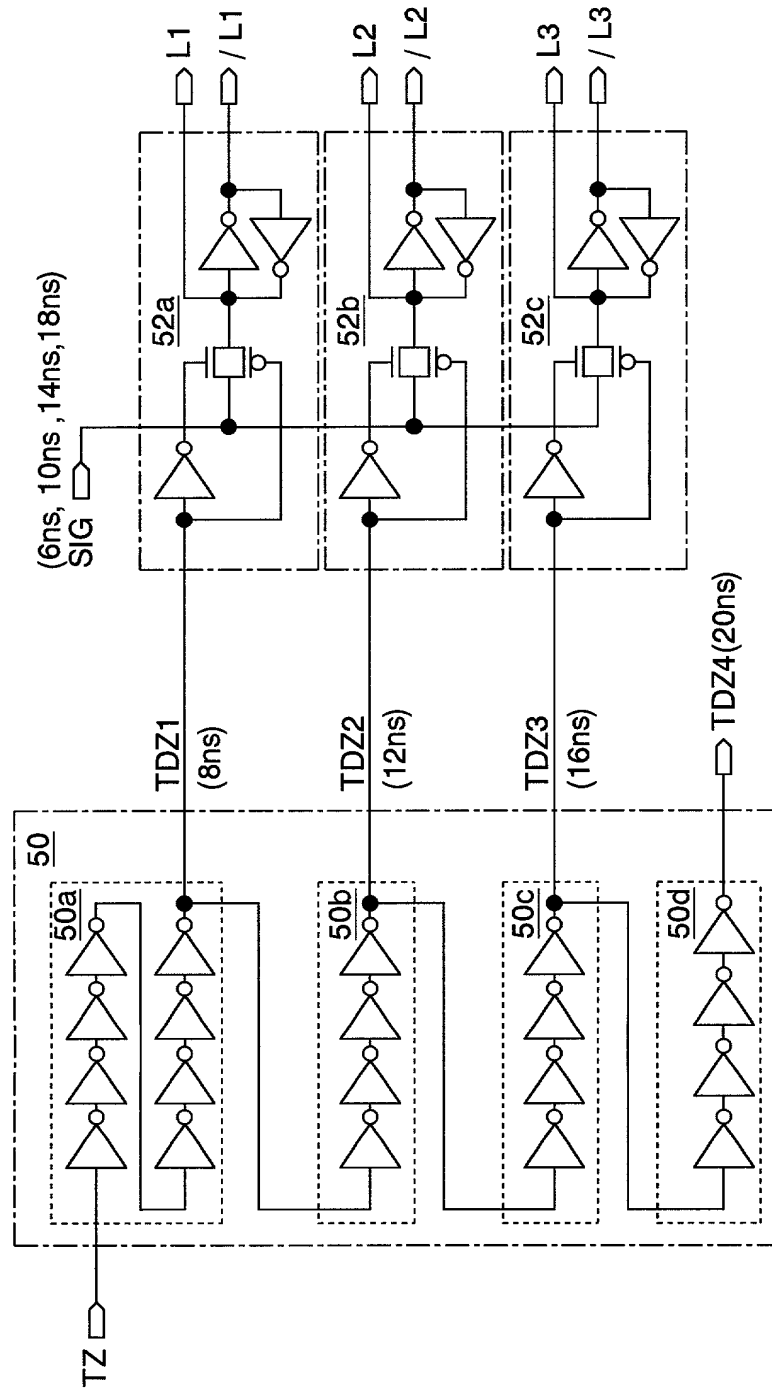


Fig. 16

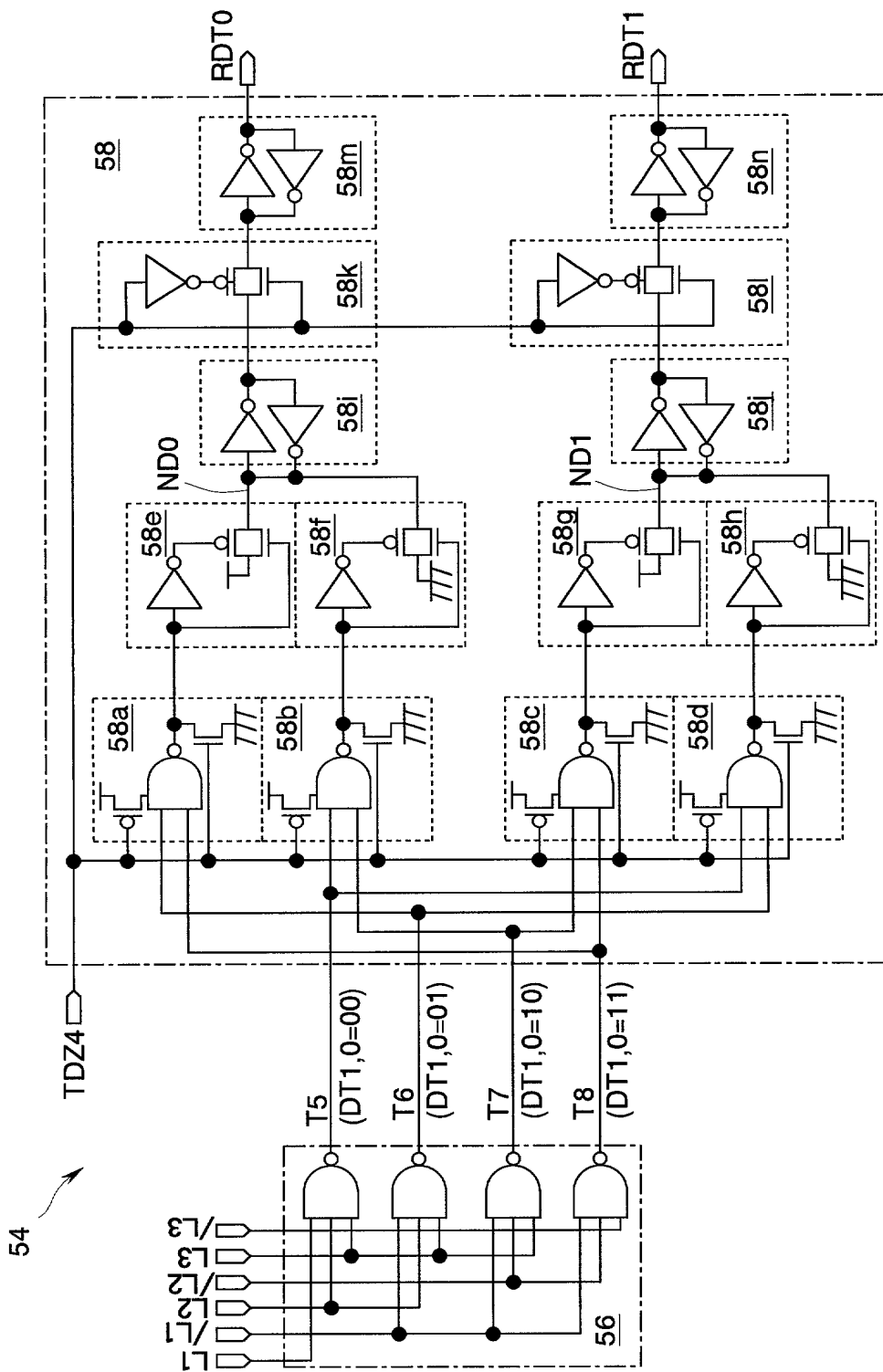


Fig. 17

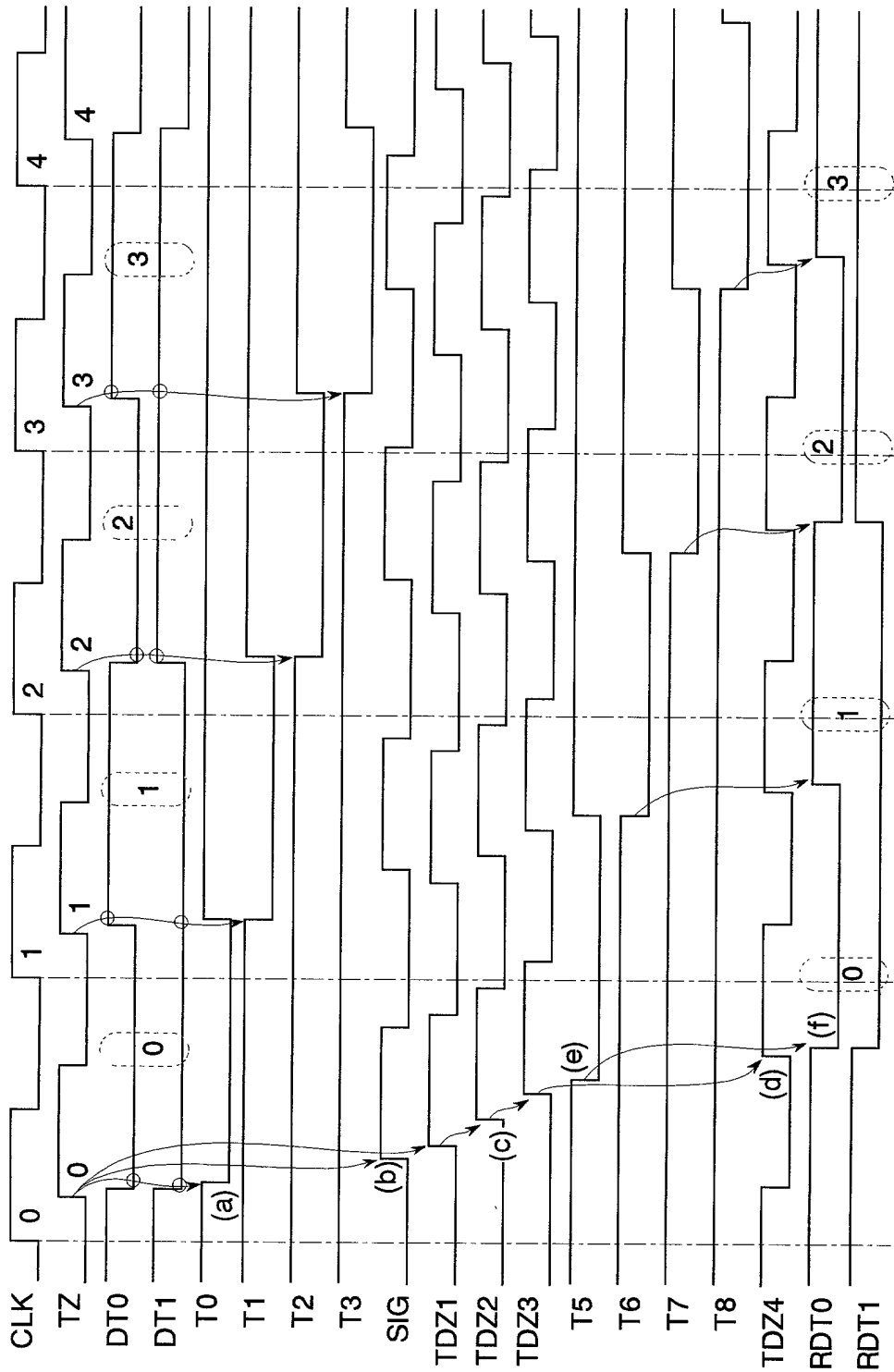


Fig. 18

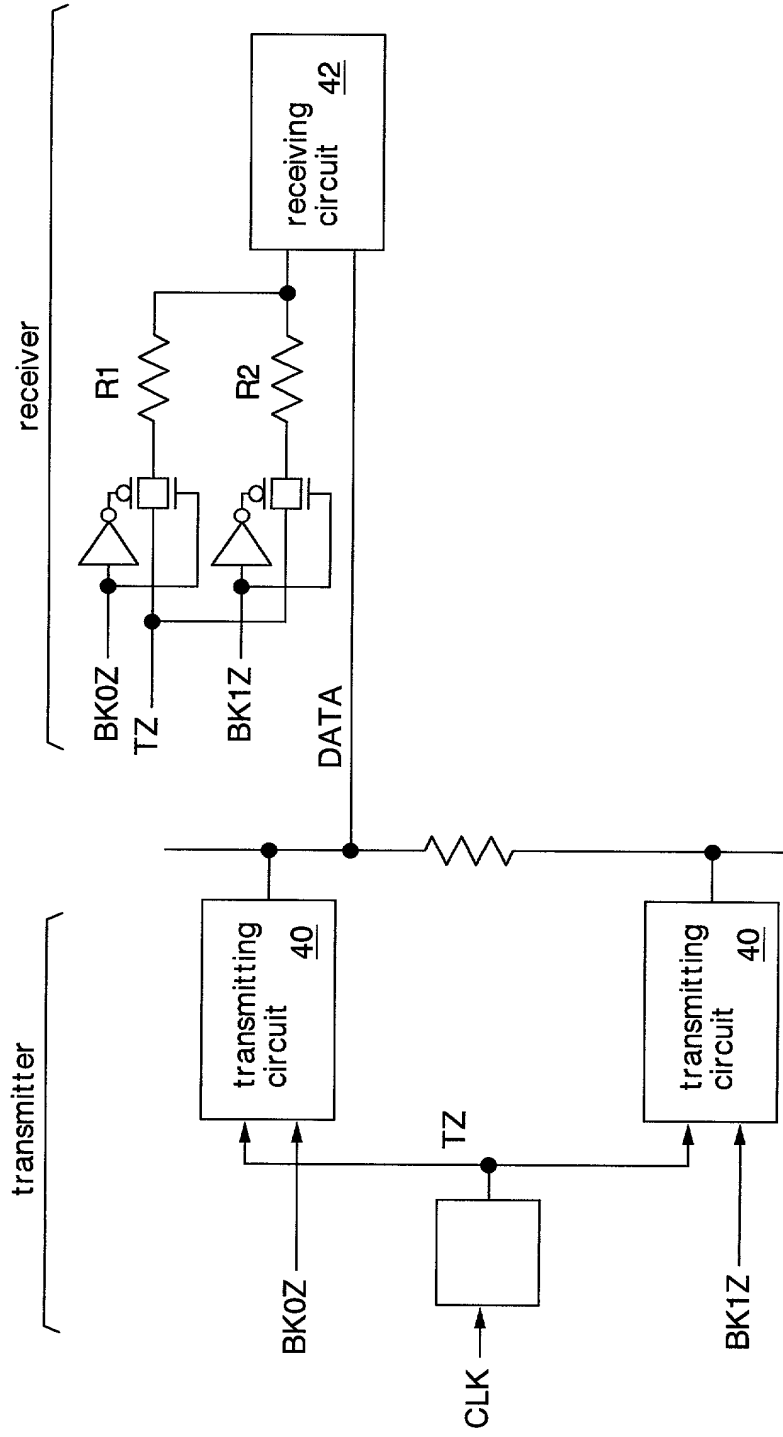


Fig. 19

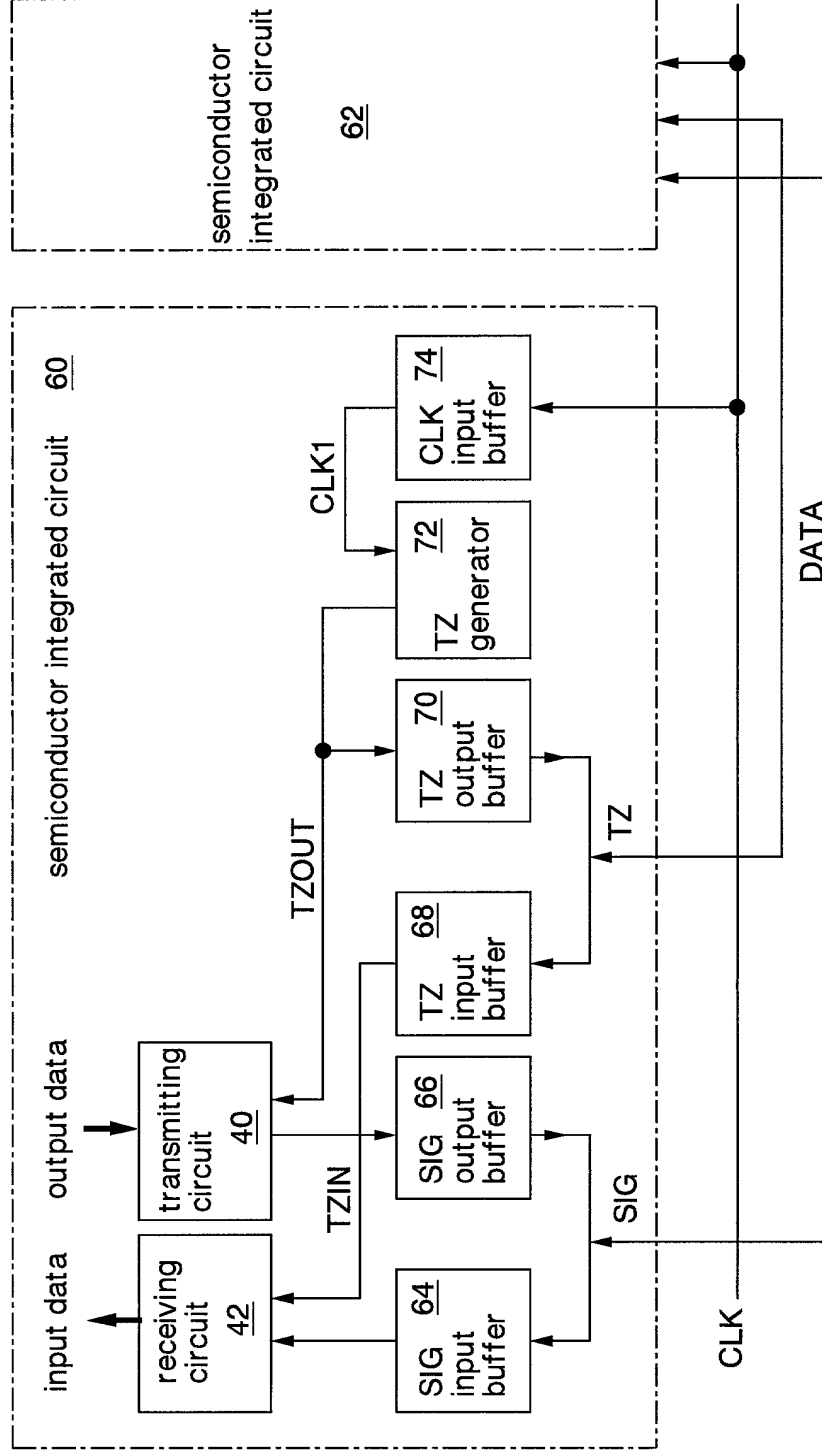


Fig. 20

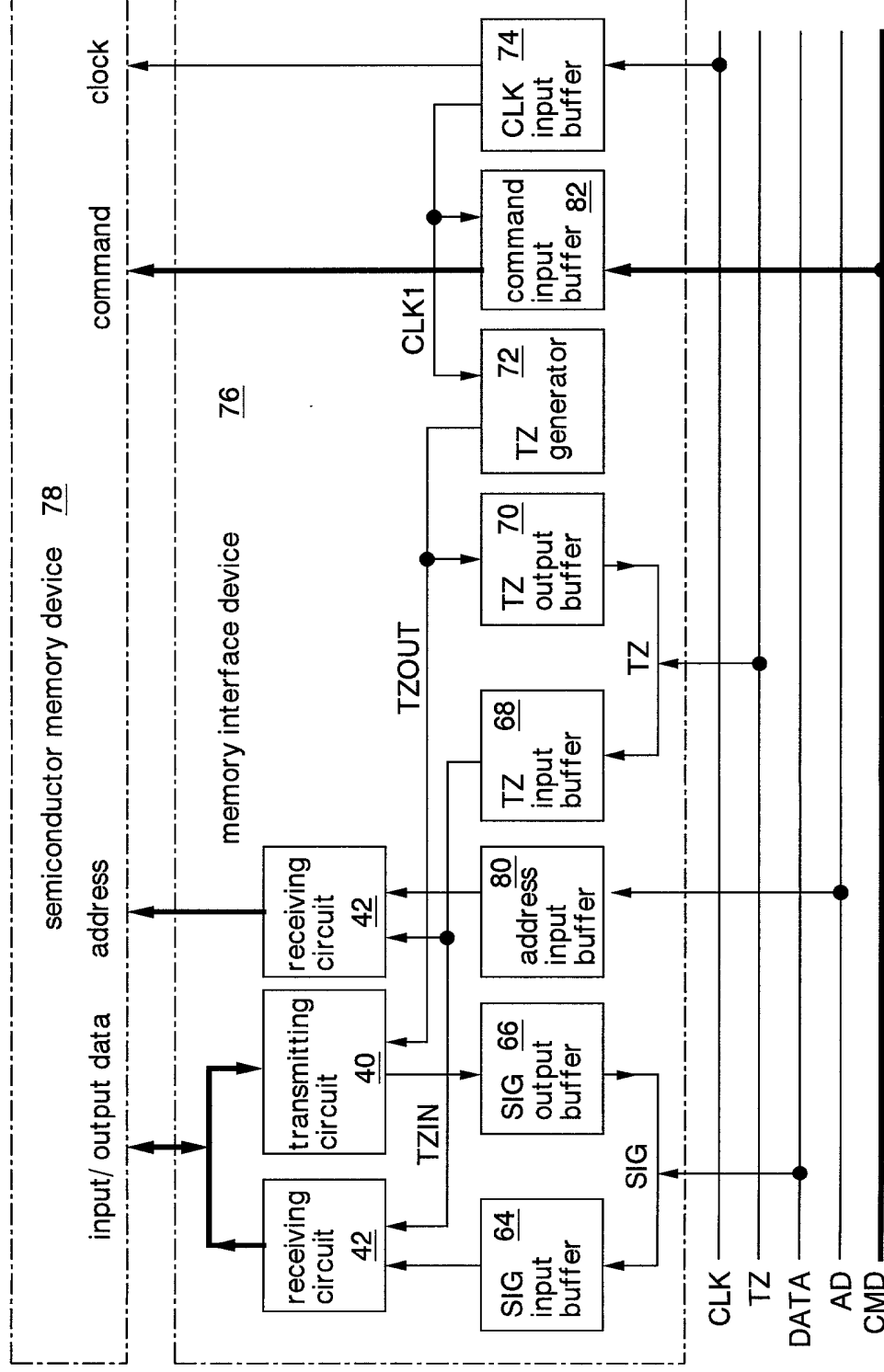


Fig. 21